SRIRAM SUNDAR SUBRAMANIAN

- Sriram Sundar Subramanian, Mahendran Gandhi 2024, 'Low Power CMOS Full Adder Cells based on Alternative Logic for High-Speed Arithmetic Applications', Informacije MIDEM - Journal of Microelectronics, Electronic Components and Materials, Vol. 54, No. 3. Impact Factor : 1.2. <u>https://doi.org/10.33180/InfMIDEM2024.303</u> (Pre-Press) (SCI Journal)
- Sriram Sundar S, Mahendran G, "CMOS full adder cells based on modified full swing restored complementary pass transistor logic for energy efficient high speed arithmetic applications", Integration, Volume 95, 2024. <u>https://doi.org/10.1016/j.vlsi.2023.102132</u>. (SCI Journal)
- Sriram Sundar S and Dr. G.Mahendran "Comparative Study of Low-Power and High Performance Flip-Flops" 8th IEEE International Conference on Information, Communication and Embedded Systems, Chennai on 29th and 30th March, 2019. ISBN: 978-1-7281-1395-1, Online ISSN: 2639-5029
- S.Somnath Sundar, K.Sathiya and S.Sriram Sundar "Intelligent Accident Prevent and Automatic Lane Clearance System for Ambulance" International Journal of Applied Engineering Research (IJAER). pp. 27751-27761, Volume 9, Number 24 (2014) Print: ISSN 0973-4562. (Scopus Indexed)
- Chen Silvia Mary and Sriram Sundar S, "Design of parallel counter architecture" in International Conference on Electronics, Communication and Information Systems 2012 (ICECI-2012) at Kalasalingam Academy of Research and Education, Krishnankoil on 02nd and 3rd November, 2012.
- 6. Sriram Sundar S and Akila P, "Low Power and High Performance System using DETSAFF", in International Conference on Futuristic Trends in Electronics Engineeing (ICFTEE-2012) at Thiruvalluvar College of Engineering and Technology, Vanthavasi on 28th and 29th January, 2012.
- S. Ravanaraja and S. Sriram Sundar, "Design of Voltage Controlled Oscillator Based Analog to Digital Converter Using Time to Digital Converter" CiiT International Journal of Digital Signal Processing, Issue : June 2011, DOI:DSP062011009, Print: ISSN 0974 – 9705 & Online: ISSN 0974 – 9594.
- Ravanaraja S and Sriram Sundar S "Design of Voltage Controlled Oscillator based Analog to Digital Converter using Time Digital Converter", in National Conference on Emerging Trends in Computer Communication and Informatics (ETCCI- 2011) at

Tamilnadu College of Engineering and Co-sponsered by CiiT International Journal, Coimbatore on 10th and 11th March 2011.

- 9. Sriram Sundar S "A Scan based Low Power Built-in Self-test Scheme for High Fault Coverage", in International Conference on Emerging Techniques in Electronics, Computing, Embedded System and VLSI Design at Padmashri Dr. Vithal Rao Vikhe Patil College Engineering, Ahmadnagar, Maharashtra on 20th and 21st March, 2008.
- Sriram Sundar S and Renuga Devi R "A Low Power BIST TPG for High Fault Coverage", in Conference on Recent Advances in Intelligent Networking – RAIN 2008, at Noorul Islam College of Engineering, Takkalai on 14th Feb, 2008.
- 11. Sriram Sundar S and Suprakash S "Wavelet based Contourlet Coding using an SPIHT like Algorithm", in Conference on Recent Trends in VLSI, Signal Processing, Communication and Embedded Technology – RVSCET 2007, at RVS College of Engineering and Technology, Dindigul on 7th April, 2007.