

SRIRAM SUNDAR SUBRAMANIAN

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2. Sriram Sundar Subramanian, Mahendran Gandhi 2024, 'Low Power CMOS Full Adder Cells based on Alternative Logic for High-Speed Arithmetic Applications', Informacije MIDEM - Journal of Microelectronics, Electronic Components and Materials, Vol. 54, No. 3. Impact Factor : 1.2. <https://doi.org/10.33180/InfMIDEM2024.303> (SCI Journal)
3. Sriram Sundar S, Mahendran G, "CMOS full adder cells based on modified full swing restored complementary pass transistor logic for energy efficient high speed arithmetic applications", Integration, Volume 95, 2024. <https://doi.org/10.1016/j.vlsi.2023.102132>. (SCI Journal)
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 4. Ponmythili Satheesh, Shinzeer C K, K G. Sathiya, Sriram Sundar S, R. Sujitha and Skb. Rathika, "Hybrid Trust-Enhanced Blockchain Scaling: A Cloud-Driven Dual-Layer Signature Verification Framework," 2025 3rd International Conference on Sustainable Computing and Smart Systems (ICSCSS), Coimbatore, India, 2025, pp. 1692-1700.
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